

MICROPROCESSORS
5th Exam/CSE/2494/Nov'24
(For 2018 Batch Onwards)

Duration: 3Hrs.

M.Marks:75

SECTION-A

Q1. Do as directed.

15x1=15

- a. How many General-Purpose registers are in 8085? Write their names.
- b. Write full form of RIM and SIM Instructions.
- c. Name Various Hardware Interrupts of 8085.
- d. Name non Maskable interrupt of 8085.
- e. What is byte size of LDA 5000H instruction?
- f. Write full form of ALP?
- g. Write the names of two modes of 8086.
- h. Which Instruction is always the last instruction of subroutine?
- i. RST stands for _____.
- j. Brief NOP Instruction.
- k. Which Signal is used to Demultiplex the Address/Data Bus?
- l. How many flags are in 8086 Microprocessor?
- m. PSW Stands for _____.
- n. What is the memory size of 8086 microprocessor?
- o. FILO stands for _____.

SECTION-B

Q2. Attempt any six questions.

6x5=30

- i. Give the basic idea of ROM, PROM, EPROM and EEPROM.
- ii. Explain any five Data Transfer Instructions of 8085 microprocessor.
- iii. Explain various steps involved in the program execution.
- iv. Describe the fetch and execute cycle.
- v. Explain Stack and its function.
- vi. Discuss the Concept of memory mapped I/O.
- vii. Write an assembly language program to 2's complement of 8-bit number.
- viii. Define address bus, data bus and control bus.

SECTION-C

Q3. Attempt any three questions.

3x10=30

- a. Draw the block diagram of 8085 and explain the function of each block.
- b. What are addressing modes of 8085? Explain their types with two examples each.
- c. Explain internal Architecture of 8086 with block diagram.
- d. Explain Hardware Interrupts of 8085 microprocessor in detail.
- e. Write short note on the following. i) 8257 (DMA controller) ii) PUSH and POP instructions

P.S.B.T.E. & I.T.